

## INA 84C640/030 8-BIT MICROCONTROLLERS WITH OCD AND VST

Microcontroller is manufactured in 42-lead plastic DIP -package 2171?.42-A.  
Device is functionally identical to the PCA84C640/030 Philips.

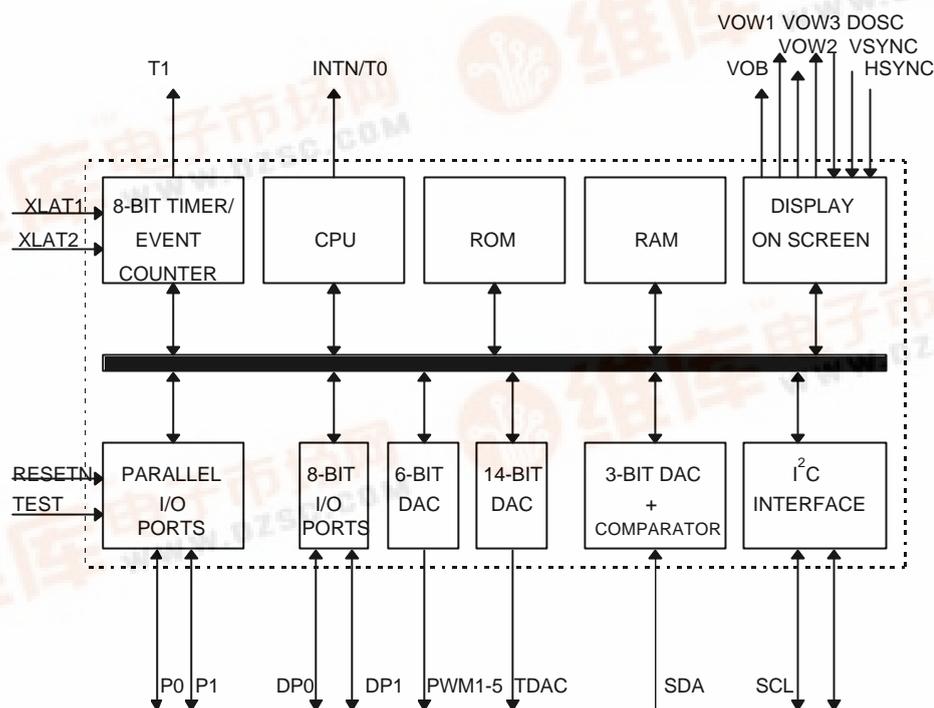
### BASIC FEATURES.

- Manufactured in 2µm silicon gate CMOS process
- TV set switch on/off
- receiving channel subbaund switching
- fine tuning receiving channel
- automatic frequency control
- PAL/SECAM switching
- volume, brightness, contrast control
- Pispay 3On Screen forility
- Operating temperature: -10 - +70°C

### BASIC ELECTRICAL CHARACTERISTICS

- Power supply voltage, V	4.5.....5.5
- Supply current, mA	16
- Clock frequency, MHz	10
- Output current LOW for P1, DPO,DP1 ports and LOW3 and VOB pins, mA	2.5
- Output current LOW for port P0, mA	7.5
- Output voltage HIGH, V	3.7

### BLOCK SCHEME



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**BELMICROSYSTEMS**



## INA 84C640/030

### PIN DESCRIPTION

PIN	SYMBOL	DESCRIPTION
1	<b>DP0.0/Tdac</b>	Derivative Port 0: quasi-bidirectional I/O line or 14-bit DAC PWM
2	<b>DP0.1/PWM1</b>	Derivative Port 1: quasi-bidirectional I/O line or 6-bit DAC PWM
3	<b>DP0.2/PWM2</b>	Derivative Port 1: quasi-bidirectional I/O line or 6-bit DAC PWM
4	<b>DP0.3/PWM3</b>	Derivative Port 1: quasi-bidirectional I/O line or 6-bit DAC PWM
5	<b>DP0.4/PWM4</b>	Derivative Port 1: quasi-bidirectional I/O line or 6-bit DAC PWM
6	<b>DP0.5/PWM5</b>	Derivative Port 1: quasi-bidirectional I/O line or 6-bit DAC PWM
7	<b>P1.0/VHF1</b>	Port 1: quasi-bidirectional I/O lines
8	<b>P1.1/VHF3</b>	Port 1: quasi-bidirectional I/O lines
9	<b>P1.7/AFC</b>	Derivative Port 1: quasi-bidirectional I/O line or comparator input with 3-bit DAC
10	<b>P1.2/VHF</b>	Port 1: quasi-bidirectional I/O lines
11	<b>P1.3/VTR</b>	Port 1: quasi-bidirectional I/O lines
12	<b>P1.4/AV</b>	Port 1: quasi-bidirectional I/O lines
13	<b>P0.0</b>	Port 0: quasi-bidirectional I/O port
14	<b>P0.1</b>	Port 0: quasi-bidirectional I/O port
15	<b>P0.2</b>	Port 0: quasi-bidirectional I/O port
16	<b>P0.3</b>	Port 0: quasi-bidirectional I/O port
17	<b>P0.4</b>	Port 0: quasi-bidirectional I/O port
18	<b>P0.5</b>	Port 0: quasi-bidirectional I/O port
19	<b>P0.6</b>	Port 0: quasi-bidirectional I/O port
20	<b>P0.7/Mdstr</b>	Port 0: quasi-bidirectional I/O port
21	<b>V<sub>SS</sub></b>	Ground
22	<b>DP1.6/VOW1</b>	Derivative Port 1: quasi-bidirectional I/O lines or character video output
23	<b>DP1.5/VOW2</b>	Derivative Port 1: quasi-bidirectional I/O lines or character video output
24	<b>VOW3</b>	Character video output of OSD
25	<b>Fbl</b>	Blanking output
26	<b>HSYNC</b>	Horizontal synchronous signal input
27	<b>VS<sub>ync</sub></b>	Vertical synchronous signal input
28	<b>DOSC</b>	Connection to RC oscillator of OSD clock
29	<b>IDENT</b>	Direct testable pin and event counter input
30	<b>Test</b>	Control input for testing and emulation mode. Ground for normal operation
31	<b>Xlat 1</b>	Oscillator output or input terminal for system clock
32	<b>Xlat 2</b>	Oscillator output or input terminal for system clock
33	<b>Reset M</b>	Initialise input, active LOW
34	<b>DP1.4/Sudl</b>	Derivative Port 1: quasi-bidirectional I/O lines
35	<b>Int/Rmot</b>	External interrupt or direct testable
36	<b>DP1.3/Sndo</b>	Derivative Port 1: quasi-bidirectional I/O lines
37	<b>DP1.2/Effect</b>	Derivative Port 1: quasi-bidirectional I/O lines
38	<b>DP1.1/P/S</b>	Derivative Port 1: quasi-bidirectional I/O lines
39	<b>DP0.7/SCL</b>	Derivative open drain I/O port or I <sup>2</sup> C-bus clock line
40	<b>DP0.6/SDA</b>	Derivative open drain I/O port or I <sup>2</sup> C-bus data line
41	<b>DP1.0/Stdby</b>	Derivative Port 1: quasi-bidirectional I/O lines
42	<b>V<sub>DD</sub></b>	Power supply

